Sheet 1 of 1

ATTY. DOCKET NO. 4590-497 INFORMATION DISCLOSURE **APPLICANT** CITATION IN AN Richard MORISSON APPLICATION FILING DATE **GROUP** herewith (PTO-1449) U.S. PATENT DOCUMENTS FILING **EXAMINER'S** CLASS **SUBCLASS** DATE NAME PATENT NO. DATE INITIALS 2001-01-02 Bult Klaas et al 6,169,510 FOREIGN PATENT DOCUMENTS EXAMINER'S INITIALS **CLASS SUBCLASS** COUNTRY PATENT NO. DATE OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Choi M et al "A 6b 1.3GSample/s A/D converter in 0.35/spl mu/m CMOS" IEEE Journal of Solid State Circuits, December 2001, pages 1847-1858 Figures 2, 13, 15 page 1847, colonne 2, ligne 27- page 1848, colonne 1, ligne 16 page 1851, colonne 2, ligne 40page 1852, cologne 2, ligne 8 Leuciuc A et al: "Active Spatial filtering for A/D Converters" IEEE International Symposium on Circuits and Systems, vol. 2, May 26, 2002-May 29, 2002 pages 11 392-11 395, figures 1, 2, 9 page 393, colonne 2, ligne 1-page 394, colonne 1, ligne 32 DATE CONSIDERED **EXAMINER** eantherne

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